

ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention

METHOD FOR CREATING A SELF-ALIGNED SOI DIODE BY
REMOVING A POLYSILICON GATE DURING PROCESSING

Submission Type : Utility Patent Filing

Application Number:

10/708912



EFS ID:

58195

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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IGEN77	Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either <us-access-code> not found in fee-xml OR actual access code not provided in the fuction call.

First Named Applicant: Kiran Chatty

Attorney Docket Number: BUR920030180US1

Timestamp: 2004-03-31 13:56:38 EDT

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File Listing:

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us-fee-sheet	BUR920030180US1-usfees.xml	2004	2004-03-31
us-fee-sheet	us-fee-sheet.xml	24912	2004-03-31
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us-assignment	BUR920030180US1-usassn.xml	2439	2004-03-31
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us-assignment	us-assignment.dtd	10898	2004-03-31
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application-body	wipo.ent	4956	2004-03-31
application-body	mathml2.dtd	54588	2004-03-31
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application-body	isoamsb.ent	3988	2004-03-31
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us-declaration	BUR920030180Dec1.tif	53060	2004-03-31
us-declaration	BUR920030180Dec2.tif	15580	2004-03-31
package-data	BUR920030180US1-pkda.xml	9028	2004-03-31
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